Docket No.: 004192.P053D

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN THE UNITED STATES PATENT AND	TRADEMARK	COFFICE
re Application for:		
) Min-hwa Chi	Examiner:	Kang, Donghee
Application No.: 09/865,929 )	Art Group:	2811
Filed: May 24, 2001 )		
For: TRANSISTOR AND LOGIC CIRCUIT ON ) THIN SILICON-ON-INSULATOR WAFERS ) BASED ON GATE INDUCED DRAIN ) LEAKAGE CURRENTS )		
Mail Stop: Issue Fee (Drawings) Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450		
TRANSMITTAL OF FORMAL DRAWINGS		
Dear Sir/Madam:		
Enclosed herewith for filing in the above-identified	U.S. patent ap	plication are the formal
drawings, Figures 1-12 (9 sheets).		
Respectfully submitte	ed,	
Date: 8/12/03  Jan Carol Little Reg. No. 41,181	OFF TAYLOR	& ZAFMAN, LLP
Certificate of Mailing:  I hereby certify that this correspondence is being deposited with the United St postage in an envelope addressed to Mail Stop: Issue Fee (Drawings), Commi 22313-1450		
on August 12, 2003 Date of Deposit		···
Luci M. Arevalo	·	
Name of Person Mailing Correspondence  Mul M. (Luvus)	just 12,2	<i>0</i> 03
Signature	<b>4</b>	